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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AE						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		
BTP	AF	"Design of an Analogue VLSI Model of an Active Cochlea," Fragniere et al. *Analog Integrated Circuits and Signal Processing*. May-June 1997. Kluwer Academic Publishers, Netherlands. Vol. 13, No. 1-2.
	AG	"Silicon Cochlea and its adaptation to spatial localization," Grech et al. *IEE Proc.-Circuits Devices Syst.* April 1999. Vol. 146, No. 2.
	AH	"ASIC Implementation of the Lyon Cochlea Model," Summerfield et al. *Digital Signal Processing 2, Estimation, VSLI*. San Francisco, CA. March 1992.
	AI	"A Low-Power Wide Dynamic-Range Analog VLSI Cochlea," Sarpehskar et al. Analog Integrated Circuits and Signal Publishing. 1998. Kluwer Academic Publishers, Boston, MA.
	AJ	"Energy-Efficient Adaptive Signal Decomposition: The Silicon and Biological Cochlea," Rahul Sarpeshkar. *Proceedings of the 1999 IEEE International Symposium on Circuits and Systems*. May-June 1999. Orlando, Florida.
BTP	AK	"A Computational Cochlear Nonlinear Preprocessing Model with Adaptive Q Circuits," Hirahara et al. *ICASSP*, 1989. P. 496-499.

EXAMINER	DATE CONSIDERED
B J D	12/7/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.